

Application of: BROWN, Steven F. et al.
Serial No.: 09/608,142
Filed: June 30, 2000
Reply to Office Action of March 21, 2008

AMENDMENTS TO THE SPECIFICATION

Please replace the first paragraph on page 1, after the heading "Cross Reference of Related Applications" with the following new paragraph:

"This application is a continuation of Application Serial No. 09/294,591, filed April 20, 1999, now U.S. Patent No. 6,377,629, (which is incorporated herein by reference), which is a continuation-in-part of Application Serial No. 08/744,629, filed November 6, 1996, now U.S. Patent No. 5,926,509, issued July 20, 1999 (which is incorporated herein by reference), which is a continuation-in-part of Application Serial No. 08/660,076, filed June 3, 1996, now U.S. Patent No. 6,184,919, which in turn is a continuation-in-part of Application Serial No. 08/177,442, filed January 5, 1994, now abandoned. The said Application Serial No. 08/744,629 also claims the benefit of Provisional Application Serial No. 60/010,741, filed January 29, 1996, and is a continuation-in-part of Application Serial No. 08/741,697, filed October 31, 1996, now U.S. Patent No. 6,150,997, which is a continuation-in-part of Application Serial No. 08/219,979, filed March 29, 1994, now U.S. Patent No. 5,576,723, issued November 19, 1996."

Replace the paragraph beginning on page 15, line 1 with the following new paragraph:

"Referring to Fig. 1a, transistor Q9 receives on its base an SB signal, across resistor 36 from computer 10, and basically serves as a buffer, providing, from its emitter, an input through resistor R35 to the base of transistor Q10 of differential amplifier 26. The emitter of transistor Q9 is D.C. biased through resistors R35 and R38 from a five-volt + terminal, designated V_{CC} throughout ~~Fig. 1~~ Figs. 1a and 1b. The base of transistor Q10 is biased through resistor R38, and capacitor C7 provides a decoupling effect across the V_{CC} terminal. The collector of transistor Q9 is connected to ground."